## HRENC-4 for imc CRONOS-SL (CRSL/HRENC-4)

High-resolution capture of up to 4 counter input channel signals, rotational encoders with enhanced resolution for two-track sine signal encoders

The plug-in module HRENC-4 for imc CRONOScompact (or configuration module for imc CRONOS-SL) serves to measure signals whose time- or frequency information is to be captured. In contrast to the case with analog channels, to actual measurement does not consist of repeated sampling at a fixed time interval. Instead, digital counters are used to determine either the count of pulses occurring or the time intervals between defined signal slope events. For the time measurement/ maximum frequency, a resolution of approx. $3.9 \mathrm{~ns}(256 \mathrm{MHz})$ is achieved.

imc CRONOS-SL-2 (back panel)

imc CRONOS-SL-2 (front panel)

When using two-track sine/cosine signal encoders, conversion to digital values for determining the rotation direction and the absolute count of increments (full periods) is performed. Additionally, detailed information about the position can be gained by analog evaluation of the sine/ cosine signal, which results in yet further increased resolution.

## Overview of available variants

| Order code | article no. | remarks |
| :--- | :---: | :--- |
| CRSL/HRENC-4-D | 11800036 | with DSUB-15 sockets |
| CRSL/HRENC-4-L | 11800037 | with LEMO sockets |

## Included accessory

| Documents |
| :--- |
| Getting started with imc CRONOScompact \& imc CRONOS-SL (one copy per delivery / system) |
| Device certificate |

Optional accessories

| DSUB-15 plug |  |  |
| :--- | :--- | :--- |
| ACC/DSUBM-ENC4 | 15-pin DSUB clamp terminal for each 2-channel pair for acquisition of <br> incremental quantities such as RPM, frequency, displacement etc. | 13500171 |
| ACC/DSUBM-ENC4-IP65 | sealed version of the ACC/DSUBM-ENC4 | 13500219 |


| DSUB-15 plug | 15-pin DSUB clamp terminal for each 2-channel pair for acquisition of <br> incremental quantities such as RPM, frequency, displacement etc. Requires <br> modifications of the incremental interfaces to a higher voltage $5 \mathrm{~V} /$ <br> 300 mA | 13500053 |
| :--- | :--- | :--- |

## Technical Specs - CRSL/HRENC-4

| Inputs, measurement modes, terminal connection |  |  |
| :---: | :---: | :---: |
| Parameter | Value | Remarks |
| Inputs | $\begin{gathered} 4+1 \\ (9 \text { tracks }) \end{gathered}$ | 4 channels with 2 tracks ( $\mathrm{X}, \mathrm{Y}$ ) each 1 index-channel, all fully conditioned |
| Measurement modes | Displacement (abs), Displacement (diff), <br> Angle (abs), Angle (diff), Event, Frequency, Speed, Velocity, Time and Puls Time Measurement | only if the sampling rate is $\leq 1 \mathrm{~ms}$ |
| Terminal connection | $\begin{gathered} 2 \times \text { DSUB- } 15 \\ \text { or } \\ 4 \times \text { LEMO } 1 \mathrm{~B} .307 \end{gathered}$ | 2 channels per DSUB (ACC/DSUBM-ENC4) <br> 1 channel per LEMO |


| General | Value | Remarks |
| :--- | :---: | :--- |
| Parameter | $\leq 50 \mathrm{kHz}$ | per channel |
| Sampling rate | 3.9 ns | Counter frequency 256 MHz <br> (primary sampling rate) |
| Measurement time resolution | 16 bit |  |
| Data resolution | $+5 \mathrm{~V}, 300 \mathrm{~mA} /$ module |  |
| Sensor supply |  |  |


| Differential-inputs |  |  |
| :---: | :---: | :---: |
| Input configuration | differential |  |
| Input voltage range (differential) | $\begin{aligned} & \pm 10 \mathrm{~V} \\ & \pm 30 \mathrm{~V} \end{aligned}$ | linear range maximum range |
| Input impedance | $50 \mathrm{k} \Omega$ |  |
| Common mode input voltage | max. $\pm 30 \mathrm{~V}$ |  |
| CMRR | 70 dB (typ.), 50 dB (min.) <br> 60 dB (typ.), 50 dB (min.) | $\begin{aligned} & \text { DC, } 50 \mathrm{~Hz} \\ & 10 \mathrm{kHz} \end{aligned}$ |
| Overvoltage protection | $\pm 50 \mathrm{~V}$ | long-term |
| Gain error | <1\% | $25^{\circ} \mathrm{C}$ |
| Offset error | <1\% | $25^{\circ} \mathrm{C}$ |
| Analog bandwidth | 500 kHz | -3 dB (full power) |
| Analog filter | Bypass (without filter), 20 kHz, 2 kHz, 200 Hz | adjustable (per channel) Butterworth, 2nd order |


| Digital Analysis (comparator) |  |  |
| :---: | :---: | :---: |
| Switching threshold | -10 V to +10 V | adjustable individual for each channels |
| Hysteresis | $0 \%$ to $40 \%$ off \|threshold|, min .100 mV | adjustable individual for each channels |
| Switching delay | 500 ns | modulation: 100 mV square wave |
| Analog analysis (ADC) |  |  |
| SIN/COS encoder analysis | $8 \times 12$ Bit A/D-converter | 8 channels of simultaneous sampling |
| Input voltage range | $\pm 1.5 \mathrm{~V}, \pm 10 \mathrm{~V}$ | (differential) |

